

Figure 2 shows current versus output code for the complete system. The LTC1404 has a digital interface consisting of the CLK and CONV input and the D_{OUT} serial digital output. The signals provide wide flexibility in allowing the part to be interfaced to most microprocessors and DSPs.

The output voltage, V_{OUT}, of the LT1787 is related to the input sense voltage by the following relationships:

$$V_{\text{SENSE}} = I_{\text{SENSE}} \cdot R_{\text{SENSE}}$$

$$V_{\text{OUT}} = 8(V_{\text{SENSE}}) + V_{\text{BIAS}}$$

Although a -3A to 2A range was selected for this illustration, other current ranges can be accommodated by a simple change in value of the sense resistor. The correct R_{SENSE} value is derived so that the product of the maximum sense current and the sense resistor value is equal to the desired maximum sense voltage (250mV for 12-bit resolution). For instance, the value of the sense resistor to sense a maximum current of 10A is 250mV/10A = 0.025Ω. The smallest measurable current is then 10A/4096 counts = 2.44mA/count. If only 10-bit resolution is desired, then the full-scale voltage can be reduced to 60mV and R_{SENSE} reduced to 0.006Ω. Ensure that the power dissipated in the sense resistor, I_{MAX}² • R_{SENSE}, does not exceed the maximum power rating of the resistor.

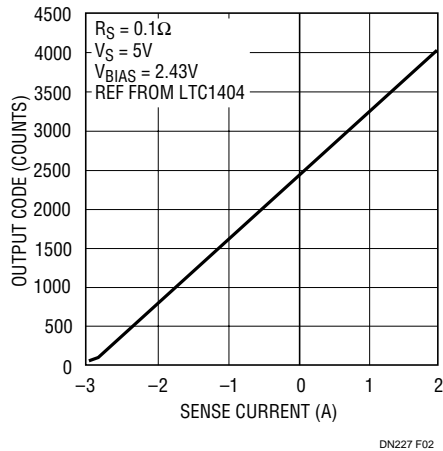


Figure 2. Current vs Output Code

Conclusion

The LT1787 high side current sense amplifier provides an easy-to-use method of sensing current with 12-bit resolution for a multiplicity of application areas. The part can operate to 60V, making it ideal for higher voltage systems in telecom or industrial applications. Additionally, the part

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can find application in battery-powered, handheld equipment and computers, where the need for gauging the amount of current consumed and/or the amount of charge remaining in the battery is critical.

Theory of Operation (See Figure 3)

Inputs V_S⁺ and V_S⁻ apply the sense voltage to matched resistors R_{G1} and R_{G2}. The opposite ends of resistors R_{G1} and R_{G2} are forced to be at equal potentials by the voltage gain of amplifier A1. The currents through R_{G1} and R_{G2} are forced to flow through transistors Q1 and Q2 and are summed at node V_{OUT} by the 1:1 current mirror. The net current from R_{G1} and R_{G2} flowing through resistor R_{OUT} gives a voltage gain of eight. Positive sense voltages result in V_{OUT} being positive with respect to pin V_{BIAS}.

Pins V_{EE}, V_{BIAS} and V_{OUT} may be connected in a variety of ways to interface with subsequent circuitry. Split supply and single supply output configurations are easily supported.

Supply current for amplifier A1 is drawn from the V_S⁻ pin. The user may choose to include this current in the monitored current (through R_{SENSE}) by careful choice of connection polarity.

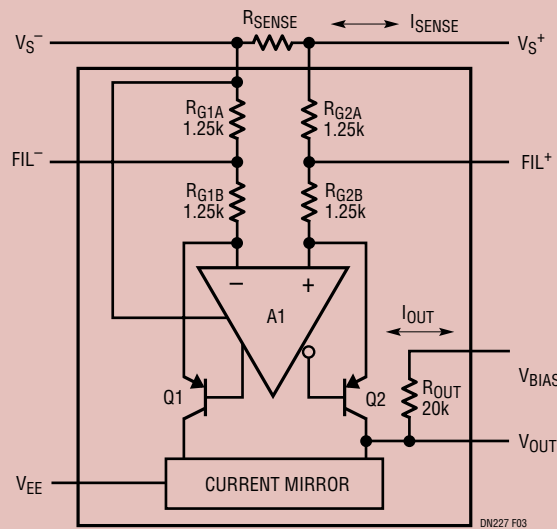


Figure 3. LT1787 Functional Diagram

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